

## HIGH LEVEL VERIFICATION

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High-level verification (HLV), or electronic system-level (ESL) verification, is the task to verify ESL designs at high abstraction level, i.e., it is the task to verify a model that represents hardware above register-transfer level (RTL) abstract level.

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Offers industry practitioners already involved with high-level synthesis an invaluable reference to high-level verification; Uses a combination of formal techniques to do scalable verification of system designs completely automatically

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High-level verification languages can also be termed as domain specific languages. Domain specific languages pack more power than the generic programming languages for those domain specific problems.

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**High- and low-level - Wikipedia**

High-level and low-level, as technical terms, are used to classify, describe and point to specific goals of a systematic operation; and are applied in a wide range of contexts, such as, for instance, in domains as widely varied as computer science and business administration.

**High-Level Verification eBook by Sudipta Kundu ...**

Read "High-Level Verification Methods and Tools for Verification of System-Level Designs" by Sudipta Kundu available from Rakuten Kobo. Sign up today and get \$5 off your first purchase. Given the growing size and heterogeneity of Systems on Chip (SOC), the design process from initial specification t

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high-level (h l v l) adj. 1. Made up of or carried out by persons having high rank or status: a high-level corporate briefing. 2. Being at an elevated level in rank or importance: a high-level official. 3. Computers Of, relating to, or being a language, such as BASIC or Pascal, in which each instruction or statement corresponds to

**High-level | Definition of High-level at Dictionary.com**

having senior authority or high status: high-level personnel. (of a programming language) based on a vocabulary of Englishlike statements for writing program code rather than the more abstract instructions typical of assembly language or machine language.